



06-26-03

1763

Filed Via Express Mail
Rec. No.: EV 332 276 994 US
On: JUN 24, 2003
By: *[Signature]*

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RECEIVED
JUN 30 2003
GROUP 1700

Attorney Docket No.: NECW 18.159

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : Atsushi NISHIZAWA
Serial No. : 09/751,979
Filed : December 29, 2000
Title : **MANUFACTURING METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT INCLUDING SIMULTANEOUS
FORMATION OF VIA HOLE REACHING METAL WIRING AND
CONCAVE GROOVE IN INTERLAYER FILM AND
SEMICONDUCTOR INTEGRATED CIRCUIT MANUFACTURED
WITH THE MANUFACTURING METHOD**
Examiner : George A. Goudreau
Group Art Unit : 1763

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR EXTENSION OF TIME

SIR:

Applicant requests that the time for taking action in this case be extended pursuant to 37 CFR 1.136(a) for:

☒ one month

☐ three months

☐ two months

☐ four months

The fee set in 37 CFR 1.17 for the extension of time is \$110.00

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☐ Fee enclosed. Please charge any additional fee required for this extension of time to Deposit Account No. 50-1290. A duplicate copy of this paper is enclosed.

☒ **Charge fee to Deposit Account No. 50-1290. A duplicate copy of this paper is enclosed.**

☐ Applicant is a small entity entitled to pay reduced fees in this application. A verified small entity statement:

☐ has been filed

☐ is enclosed

Also enclosed is a:

☒ **Response to Official Action**

☐ Notice of Appeal

☐ Appeal Brief

Respectfully submitted,



Michael I. Markowitz
Reg. No. 30,659

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New York, New York 10022-2585
(212) 940-8687
Docket No.: NECW 18.159